Patent Application of

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for

### TITLE: FASTER IMAGE PROCESSING

*THIS APPLN CLAIMS BENEFIT OF 60/176,346. 01/14/2000*

### BACKGROUND—FIELD OF THE INVENTION

This invention relates to image processing.

### BACKGROUND—RELATED TECHNOLOGY

**ANSI Standard C "memcpy" Function**

A given computer hardware architecture will have an optimal means of copying a block of data from one location in a memory to another location. Complex Instruction Set Computing (CISC) architectures implement instructions that over a number of CPU cycles move a block of data. Reduced Instruction Set Computing (RISC) architectures optimize the instruction set to process each instruction in one or two CPU cycles but also included instructions that can be used to implement a short routine that will accomplish the block move in an optimal manner. An efficient routine for copying a block of data can be implemented for each specific computer architecture.